

PATENT NUMBER and
ISSUE DATE

10/044402
01/11/02

U.S. UTILITY Patent Application

APPL NUM 10044402	FILING DATE 01/11/2002	CLASS 118	SUBCLASS 52	GAU 1734	EXAMINER <i>Goddard</i>
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**APPLICANTS: Menut Olivier; Gris Yvon;

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Brock

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**CONTINUING DATA VERIFIED:

NOTE DISCLAIMER
The term of this patent shall not extend beyond the expiration date of Pat. No. 1,537,813.

** FOREIGN APPLICATIONS VERIFIED:

FRANCE 0100414 01/12/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO	
35 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	00-GR1-239	
Verified and Acknowledged Examiners's initials			
TITLE : Process for fabricating a single-crystal substrate and integrated circuit comprising such a substrate			
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Primary Examiner	
		Application Examiner	
<input checked="" type="checkbox"/> TERMINAL		PREPARED FOR ISSUE	
DISCLAIMER			
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